

| COURSE NAME                                                                      | TW                                  | OR       | PR       | CAA     | ISE      | ESE      | Tot    | Tot% | C CR | E CR | Grd                            | GP | CP | P&R | ORD |
|----------------------------------------------------------------------------------|-------------------------------------|----------|----------|---------|----------|----------|--------|------|------|------|--------------------------------|----|----|-----|-----|
| SEM.: III                                                                        |                                     |          |          |         |          |          |        |      |      |      |                                |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *                                   | -        | -        | 8 /10.0 | 3 /30.0  | Ab /60.0 | 11/100 | 11   | 3.0  | 0    | F                              | 0  | 0  | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | *                                   | -        | -        | 7 /10.0 | 12 /30.0 | 3 /60.0  | 22/100 | 22   | 3.0  | 0    | F                              | 0  | 0  | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | *                                   | -        | 18 /30.0 | -       | -        | -        | 18/30  | 60   | 1.0  | 1    | B+                             | 7  | 7  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *                                   | -        | -        | 8 /10.0 | 6 /30.0  | 0 /60.0  | 14/100 | 14   | 3.0  | 0    | F                              | 0  | 0  | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *                                   | -        | 4 /30.0  | -       | -        | -        | 4/30   | 13   | 1.0  | 0    | F                              | 0  | 0  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *                                   | -        | -        | 4 /10.0 | 1 /30.0  | 0 /60.0  | 5/100  | 5    | 3.0  | 0    | F                              | 0  | 0  | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *                                   | -        | 12 /30.0 | -       | -        | -        | 12/30  | 40   | 1.0  | 1    | P                              | 4  | 4  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | *                                   | -        | -        | 6 /10.0 | -        | 28 /60.0 | 34/70  | 49   | 2.0  | 2    | C                              | 5  | 10 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | *                                   | 25 /30.0 | -        | -       | -        | -        | 25/30  | 83   | 1.0  | 1    | A+                             | 9  | 9  | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *                                   | 19 /30.0 | 28 /30.0 | -       | 7 /10.0  | -        | 54/70  | 77   | 2.0  | 2    | A                              | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *                                   | 27 /30.0 | 26 /30.0 | -       | 9 /10.0  | -        | 62/70  | 89   | 2.0  | 2    | A+                             | 9  | 18 | --  |     |
| SGPA1 : -                                                                        |                                     |          |          |         |          |          |        |      |      |      |                                |    |    |     |     |
| CGPA :                                                                           | Cumulative Result Status : A.T.K.T. |          |          |         |          |          |        |      |      |      | TOTAL EARNED CREDIT : 9.0/22.0 |    |    |     |     |
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| COURSE NAME                                                                                                                               | TW | OR       | PR       | CAA      | ISE      | ESE         | Tot    | Tot% | C CR | E CR | Grd | GP | CP | P&R | ORD |
|-------------------------------------------------------------------------------------------------------------------------------------------|----|----------|----------|----------|----------|-------------|--------|------|------|------|-----|----|----|-----|-----|
| <div> <div>PRN NO. : EN24102005</div> <div>NAME OF CANDIDATE : BHOSALE RUSHIKESH LAXMAN</div> <div>MOTHER NAME : SHAKUBAI</div> </div>    |    |          |          |          |          |             |        |      |      |      |     |    |    |     |     |
| SEM.: III                                                                                                                                 |    |          |          |          |          |             |        |      |      |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                                                                                       | *  | -        | -        | 10 /10.0 | 20 /30.0 | 28 /60.0    | 58/100 | 58   | 3.0  | 3    | B   | 6  | 18 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                                                                                 | *  | -        | -        | 8 /10.0  | 6 /30.0  | 33 /60.0    | 47/100 | 47   | 3.0  | 3    | C   | 5  | 15 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                                                                                 | *  | -        | 24 /30.0 | -        | -        | -           | 24/30  | 80   | 1.0  | 1    | A+  | 9  | 9  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                                                                            | *  | -        | -        | 8 /10.0  | 15 /30.0 | 24 \$ /60.0 | 47/100 | 47   | 3.0  | 3    | C   | 5  | 15 | --  | \$  |
| ET24202PR DIGITAL LOGIC DESIGN                                                                                                            | *  | -        | 15 /30.0 | -        | -        | -           | 15/30  | 50   | 1.0  | 1    | B   | 6  | 6  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                                                                             | *  | -        | -        | 6 /10.0  | 10 /30.0 | 40 /60.0    | 56/100 | 56   | 3.0  | 3    | B   | 6  | 18 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                                                                             | *  | -        | 16 /30.0 | -        | -        | -           | 16/30  | 53   | 1.0  | 1    | B   | 6  | 6  | --  |     |
| IT24053TH DATA STRUCTURE                                                                                                                  | *  | -        | -        | 5 /10.0  | -        | 32 /60.0    | 37/70  | 53   | 2.0  | 2    | B   | 6  | 12 | --  |     |
| IT24053PR DATA STRUCTURE                                                                                                                  | *  | 27 /30.0 | -        | -        | -        | -           | 27/30  | 90   | 1.0  | 1    | O   | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                                                                                     | *  | 24 /30.0 | 23 /30.0 | -        | 9 /10.0  | -           | 56/70  | 80   | 2.0  | 2    | A+  | 9  | 18 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                                                                                   | *  | 25 /30.0 | 25 /30.0 | -        | 8 /10.0  | -           | 58/70  | 83   | 2.0  | 2    | A+  | 9  | 18 | --  |     |
| SGPA1 : 6.59                                                                                                                              |    |          |          |          |          |             |        |      |      |      |     |    |    |     |     |
| <div> <div>CGPA :</div> <div>Cumulative Result Status : A.T.K.T.</div> <div>TOTAL EARNED CREDIT : 22.0/22.0 [\$ Ordinance 1]</div> </div> |    |          |          |          |          |             |        |      |      |      |     |    |    |     |     |
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| PRN NO. : EN24102008                                                             |   | NAME OF CANDIDATE : CHORMALE SAKSHI BAPURAV |          |          |          | MOTHER NAME : ANITA |          |        |      |      |      |                                 |    |    |     |     |
|----------------------------------------------------------------------------------|---|---------------------------------------------|----------|----------|----------|---------------------|----------|--------|------|------|------|---------------------------------|----|----|-----|-----|
| COURSE NAME                                                                      |   | TW                                          | OR       | PR       | CAA      | ISE                 | ESE      | Tot    | Tot% | C CR | E CR | Grd                             | GP | CP | P&R | ORD |
| SEM.: III                                                                        |   |                                             |          |          |          |                     |          |        |      |      |      |                                 |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | * | -                                           | -        | -        | 10 /10.0 | 29 /30.0            | 46 /60.0 | 85/100 | 85   | 3.0  | 3    | A+                              | 9  | 27 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | * | -                                           | -        | -        | 9 /10.0  | 12 /30.0            | 47 /60.0 | 68/100 | 68   | 3.0  | 3    | B+                              | 7  | 21 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | * | -                                           | -        | 22 /30.0 | -        | -                   | -        | 22/30  | 73   | 1.0  | 1    | A                               | 8  | 8  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | * | -                                           | -        | -        | 8 /10.0  | 24 /30.0            | 43 /60.0 | 75/100 | 75   | 3.0  | 3    | A                               | 8  | 24 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | * | -                                           | -        | 25 /30.0 | -        | -                   | -        | 25/30  | 83   | 1.0  | 1    | A+                              | 9  | 9  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | * | -                                           | -        | -        | 5 /10.0  | 16 /30.0            | 39 /60.0 | 60/100 | 60   | 3.0  | 3    | B+                              | 7  | 21 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | * | -                                           | 14 /30.0 | -        | -        | -                   | -        | 14/30  | 47   | 1.0  | 1    | C                               | 5  | 5  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | * | -                                           | -        | -        | 8 /10.0  | -                   | 46 /60.0 | 54/70  | 77   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | * | 25 /30.0                                    | -        | -        | -        | -                   | -        | 25/30  | 83   | 1.0  | 1    | A+                              | 9  | 9  | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | * | 21 /30.0                                    | 21 /30.0 | -        | 8 /10.0  | -                   | -        | 50/70  | 71   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | * | 26 /30.0                                    | 24 /30.0 | -        | 9 /10.0  | -                   | -        | 59/70  | 84   | 2.0  | 2    | A+                              | 9  | 18 | --  |     |
| SGPA1 : 7.91                                                                     |   |                                             |          |          |          |                     |          |        |      |      |      |                                 |    |    |     |     |
| CGPA : 7.58                                                                      |   | Cumulative Result Status : PASS             |          |          |          |                     |          |        |      |      |      | TOTAL EARNED CREDIT : 22.0/22.0 |    |    |     |     |
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| PRN NO. : EN24102010                                                             |    | NAME OF CANDIDATE : DONI PRATIK BABURAO |          |          | MOTHER NAME : SANGEETA |                                                  |             |        |      |      |     |    |    |     |     |  |
|----------------------------------------------------------------------------------|----|-----------------------------------------|----------|----------|------------------------|--------------------------------------------------|-------------|--------|------|------|-----|----|----|-----|-----|--|
| COURSE NAME                                                                      | TW | OR                                      | PR       | CAA      | ISE                    | ESE                                              | Tot         | Tot%   | C CR | E CR | Grd | GP | CP | P&R | ORD |  |
| SEM.: III                                                                        |    |                                         |          |          |                        |                                                  |             |        |      |      |     |    |    |     |     |  |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                       | -        | -        | 10 /10.0               | 14 /30.0                                         | 35 /60.0    | 59/100 | 59   | 3.0  | 3   | B  | 6  | 18  | --  |  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                       | -        | -        | 8 /10.0                | 12 /30.0                                         | 32 /60.0    | 52/100 | 52   | 3.0  | 3   | B  | 6  | 18  | --  |  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                       | -        | 17 /30.0 | -                      | -                                                | -           | 17/30  | 57   | 1.0  | 1   | B  | 6  | 6   | --  |  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                       | -        | -        | 7 /10.0                | 26 /30.0                                         | 24 \$ /60.0 | 57/100 | 57   | 3.0  | 3   | B  | 6  | 18  | --  |  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                       | -        | 24 /30.0 | -                      | -                                                | -           | 24/30  | 80   | 1.0  | 1   | A+ | 9  | 9   | --  |  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                       | -        | -        | 8 /10.0                | 16 /30.0                                         | 46 /60.0    | 70/100 | 70   | 3.0  | 3   | A  | 8  | 24  | --  |  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                       | 15 /30.0 | -        | -                      | -                                                | -           | 15/30  | 50   | 1.0  | 1   | B  | 6  | 6   | --  |  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                       | -        | -        | 5 /10.0                | -                                                | 44 /60.0    | 49/70  | 70   | 2.0  | 2   | A  | 8  | 16  | --  |  |
| IT24053PR DATA STRUCTURE                                                         | *  | 21 /30.0                                | -        | -        | -                      | -                                                | -           | 21/30  | 70   | 1.0  | 1   | A  | 8  | 8   | --  |  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 19 /30.0                                | 20 /30.0 | -        | 8 /10.0                | -                                                | -           | 47/70  | 67   | 2.0  | 2   | B+ | 7  | 14  | --  |  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 25 /30.0                                | 25 /30.0 | -        | 7 /10.0                | -                                                | -           | 57/70  | 81   | 2.0  | 2   | A+ | 9  | 18  | --  |  |
| SGPA1 : 7.05                                                                     |    |                                         |          |          |                        |                                                  |             |        |      |      |     |    |    |     |     |  |
| CGPA : 7.44                                                                      |    | Cumulative Result Status : PASS         |          |          |                        | TOTAL EARNED CREDIT : 22.0/22.0 [\$ Ordinance 1] |             |        |      |      |     |    |    |     |     |  |
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|----------------------------------------------------------------------------------|----|----------------------------------------|----------|----------|-----------------------|----------|----------|--------|------|---------------------------------|-----|----|----|-----|-----|--|
| PRN NO. : EN24102012                                                             |    | NAME OF CANDIDATE : GADE SAYALI DHIRAJ |          |          | MOTHER NAME : MANISHA |          |          |        |      |                                 |     |    |    |     |     |  |
| COURSE NAME                                                                      | TW | OR                                     | PR       | CAA      | ISE                   | ESE      | Tot      | Tot%   | C CR | E CR                            | Grd | GP | CP | P&R | ORD |  |
| SEM.: III                                                                        |    |                                        |          |          |                       |          |          |        |      |                                 |     |    |    |     |     |  |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                      | -        | -        | 10 /10.0              | 30 /30.0 | 59 /60.0 | 99/100 | 99   | 3.0                             | 3   | O  | 10 | 30  | --  |  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                      | -        | -        | 8 /10.0               | 26 /30.0 | 48 /60.0 | 82/100 | 82   | 3.0                             | 3   | A+ | 9  | 27  | --  |  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                      | -        | 20 /30.0 | -                     | -        | 20/30    | 67     | 1.0  | 1                               | B+  | 7  | 7  | --  |     |  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                      | -        | -        | 9 /10.0               | 29 /30.0 | 24 /60.0 | 62/100 | 62   | 3.0                             | 3   | B+ | 7  | 21  | --  |  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                      | -        | 18 /30.0 | -                     | -        | 18/30    | 60     | 1.0  | 1                               | B+  | 7  | 7  | --  |     |  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                      | -        | -        | 8 /10.0               | 22 /30.0 | 47 /60.0 | 77/100 | 77   | 3.0                             | 3   | A  | 8  | 24  | --  |  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                      | 23 /30.0 | -        | -                     | -        | 23/30    | 77     | 1.0  | 1                               | A   | 8  | 8  | --  |     |  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                      | -        | -        | 7 /10.0               | -        | 55 /60.0 | 62/70  | 89   | 2.0                             | 2   | A+ | 9  | 18  | --  |  |
| IT24053PR DATA STRUCTURE                                                         | *  | 28 /30.0                               | -        | -        | -                     | -        | 28/30    | 93     | 1.0  | 1                               | O   | 10 | 10 | --  |     |  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 22 /30.0                               | 20 /30.0 | -        | 9 /10.0               | -        | 51/70    | 73     | 2.0  | 2                               | A   | 8  | 16 | --  |     |  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 25 /30.0                               | 25 /30.0 | -        | 8 /10.0               | -        | 58/70    | 83     | 2.0  | 2                               | A+  | 9  | 18 | --  |     |  |
| SGPA1 : 8.45                                                                     |    |                                        |          |          |                       |          |          |        |      |                                 |     |    |    |     |     |  |
| CGPA : 8.77                                                                      |    | Cumulative Result Status : PASS        |          |          |                       |          |          |        |      | TOTAL EARNED CREDIT : 22.0/22.0 |     |    |    |     |     |  |
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| PRN NO. : EN24102017                                                             |   | NAME OF CANDIDATE : HOSING SWARAJ AMARENDRA |          |          |          | MOTHER NAME : SONIYA |          |        |      |      |      |                                 |    |    |     |     |
|----------------------------------------------------------------------------------|---|---------------------------------------------|----------|----------|----------|----------------------|----------|--------|------|------|------|---------------------------------|----|----|-----|-----|
| COURSE NAME                                                                      |   | TW                                          | OR       | PR       | CAA      | ISE                  | ESE      | Tot    | Tot% | C CR | E CR | Grd                             | GP | CP | P&R | ORD |
| SEM.: III                                                                        |   |                                             |          |          |          |                      |          |        |      |      |      |                                 |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | * | -                                           | -        | -        | 10 /10.0 | 24 /30.0             | 58 /60.0 | 92/100 | 92   | 3.0  | 3    | O                               | 10 | 30 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | * | -                                           | -        | -        | 9 /10.0  | 16 /30.0             | 29 /60.0 | 54/100 | 54   | 3.0  | 3    | B                               | 6  | 18 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | * | -                                           | -        | 21 /30.0 | -        | -                    | -        | 21/30  | 70   | 1.0  | 1    | A                               | 8  | 8  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | * | -                                           | -        | -        | 9 /10.0  | 23 /30.0             | 24 /60.0 | 56/100 | 56   | 3.0  | 3    | B                               | 6  | 18 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | * | -                                           | -        | 18 /30.0 | -        | -                    | -        | 18/30  | 60   | 1.0  | 1    | B+                              | 7  | 7  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | * | -                                           | -        | -        | 8 /10.0  | 14 /30.0             | 43 /60.0 | 65/100 | 65   | 3.0  | 3    | B+                              | 7  | 21 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | * | -                                           | 24 /30.0 | -        | -        | -                    | -        | 24/30  | 80   | 1.0  | 1    | A+                              | 9  | 9  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | * | -                                           | -        | -        | 5 /10.0  | -                    | 49 /60.0 | 54/70  | 77   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | * | 30 /30.0                                    | -        | -        | -        | -                    | -        | 30/30  | 100  | 1.0  | 1    | O                               | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | * | 20 /30.0                                    | 23 /30.0 | -        | 9 /10.0  | -                    | -        | 52/70  | 74   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | * | 28 /30.0                                    | 23 /30.0 | -        | 9 /10.0  | -                    | -        | 60/70  | 86   | 2.0  | 2    | A+                              | 9  | 18 | --  |     |
| SGPA1 : 7.77                                                                     |   |                                             |          |          |          |                      |          |        |      |      |      |                                 |    |    |     |     |
| CGPA : 8.19                                                                      |   | Cumulative Result Status : PASS             |          |          |          |                      |          |        |      |      |      | TOTAL EARNED CREDIT : 22.0/22.0 |    |    |     |     |
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| PRN NO. : EN24102019                                                             |    | NAME OF CANDIDATE : JADHAV UTKARSH DEEPAK |          |          |         | MOTHER NAME : ASHA |          |        |      |      |     |                                 |    |     |     |  |
|----------------------------------------------------------------------------------|----|-------------------------------------------|----------|----------|---------|--------------------|----------|--------|------|------|-----|---------------------------------|----|-----|-----|--|
| COURSE NAME                                                                      | TW | OR                                        | PR       | CAA      | ISE     | ESE                | Tot      | Tot%   | C CR | E CR | Grd | GP                              | CP | P&R | ORD |  |
| SEM.: III                                                                        |    |                                           |          |          |         |                    |          |        |      |      |     |                                 |    |     |     |  |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                         | -        | -        | 7 /10.0 | 15 /30.0           | 40 /60.0 | 62/100 | 62   | 3.0  | 3   | B+                              | 7  | 21  | --  |  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                         | -        | -        | 9 /10.0 | 9 /30.0            | 27 /60.0 | 45/100 | -    | 3.0  | 3   | C                               | 5  | 15  | --  |  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                         | -        | 22 /30.0 | -       | -                  | -        | 22/30  | 73   | 1.0  | 1   | A                               | 8  | 8   | --  |  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | -        | 8 /10.0 | 7 /30.0            | 25 /60.0 | 40/100 | 40   | 3.0  | 3   | P                               | 4  | 12  | --  |  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | 13 /30.0 | -       | -                  | -        | 13/30  | 43   | 1.0  | 1   | P                               | 4  | 4   | --  |  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                         | -        | -        | 6 /10.0 | 9 /30.0            | 30 /60.0 | 45/100 | -    | 3.0  | 3   | C                               | 5  | 15  | --  |  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                         | 14 /30.0 | -        | -       | -                  | -        | 14/30  | 47   | 1.0  | 1   | C                               | 5  | 5   | --  |  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                         | -        | -        | 5 /10.0 | -                  | 41 /60.0 | 46/70  | 66   | 2.0  | 2   | B+                              | 7  | 14  | --  |  |
| IT24053PR DATA STRUCTURE                                                         | *  | 18 /30.0                                  | -        | -        | -       | -                  | -        | 18/30  | 60   | 1.0  | 1   | B+                              | 7  | 7   | --  |  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 22 /30.0                                  | 22 /30.0 | -        | 7 /10.0 | -                  | -        | 51/70  | 73   | 2.0  | 2   | A                               | 8  | 16  | --  |  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 27 /30.0                                  | 23 /30.0 | -        | 8 /10.0 | -                  | -        | 58/70  | 83   | 2.0  | 2   | A+                              | 9  | 18  | --  |  |
| SGPA1 : 6.14                                                                     |    |                                           |          |          |         |                    |          |        |      |      |     |                                 |    |     |     |  |
| CGPA : 6.81                                                                      |    | Cumulative Result Status : PASS           |          |          |         |                    |          |        |      |      |     | TOTAL EARNED CREDIT : 22.0/22.0 |    |     |     |  |
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| PRN NO. : EN24102025                                                             |    | NAME OF CANDIDATE : KHARAT PRERANA BIRA |          |          | MOTHER NAME : ANITA |          |          |        |      |      |     |                                 |    |     |     |  |
|----------------------------------------------------------------------------------|----|-----------------------------------------|----------|----------|---------------------|----------|----------|--------|------|------|-----|---------------------------------|----|-----|-----|--|
| COURSE NAME                                                                      | TW | OR                                      | PR       | CAA      | ISE                 | ESE      | Tot      | Tot%   | C CR | E CR | Grd | GP                              | CP | P&R | ORD |  |
| SEM.: III                                                                        |    |                                         |          |          |                     |          |          |        |      |      |     |                                 |    |     |     |  |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                       | -        | -        | 10 /10.0            | 27 /30.0 | 60 /60.0 | 97/100 | 97   | 3.0  | 3   | O                               | 10 | 30  | --  |  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                       | -        | -        | 8 /10.0             | 20 /30.0 | 36 /60.0 | 64/100 | 64   | 3.0  | 3   | B+                              | 7  | 21  | --  |  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                       | -        | 2 /30.0  | -                   | -        | 2/30     | 7      | 1.0  | 0    | F   | 0                               | 0  | --  |     |  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                       | -        | -        | 8 /10.0             | 27 /30.0 | 29 /60.0 | 64/100 | 64   | 3.0  | 3   | B+                              | 7  | 21  | --  |  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                       | -        | 13 /30.0 | -                   | -        | 13/30    | 43     | 1.0  | 1    | P   | 4                               | 4  | --  |     |  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                       | -        | -        | 5 /10.0             | 17 /30.0 | 48 /60.0 | 70/100 | 70   | 3.0  | 3   | A                               | 8  | 24  | --  |  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                       | 12 /30.0 | -        | -                   | -        | 12/30    | 40     | 1.0  | 1    | P   | 4                               | 4  | --  |     |  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                       | -        | -        | 4 /10.0             | -        | 39 /60.0 | 43/70  | 61   | 2.0  | 2   | B+                              | 7  | 14  | --  |  |
| IT24053PR DATA STRUCTURE                                                         | *  | 21 /30.0                                | -        | -        | -                   | -        | 21/30    | 70     | 1.0  | 1    | A   | 8                               | 8  | --  |     |  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 21 /30.0                                | 25 /30.0 | -        | 8 /10.0             | -        | 54/70    | 77     | 2.0  | 2    | A   | 8                               | 16 | --  |     |  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 24 /30.0                                | 25 /30.0 | -        | 8 /10.0             | -        | 57/70    | 81     | 2.0  | 2    | A+  | 9                               | 18 | --  |     |  |
| SGPA1 : -                                                                        |    |                                         |          |          |                     |          |          |        |      |      |     |                                 |    |     |     |  |
| CGPA :                                                                           |    | Cumulative Result Status : A.T.K.T.     |          |          |                     |          |          |        |      |      |     | TOTAL EARNED CREDIT : 21.0/22.0 |    |     |     |  |
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|----------------------------------------------------------------------------------|----|----------------------------------------|----------|----------|----------|----------------------|----------|--------|------|---------------------------------|-----|----|----|-----|-----|
| PRN NO. : EN24102033                                                             |    | NAME OF CANDIDATE : MULLA ARSIYA AKBAR |          |          |          | MOTHER NAME : YASMIN |          |        |      |                                 |     |    |    |     |     |
| COURSE NAME                                                                      | TW | OR                                     | PR       | CAA      | ISE      | ESE                  | Tot      | Tot%   | C CR | E CR                            | Grd | GP | CP | P&R | ORD |
| SEM.: III                                                                        |    |                                        |          |          |          |                      |          |        |      |                                 |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                      | -        | -        | 10 /10.0 | 30 /30.0             | 58 /60.0 | 98/100 | 98   | 3.0                             | 3   | O  | 10 | 30  | --  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                      | -        | -        | 9 /10.0  | 25 /30.0             | 48 /60.0 | 82/100 | 82   | 3.0                             | 3   | A+ | 9  | 27  | --  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                      | -        | 24 /30.0 | -        | -                    | 24/30    | 80     | 1.0  | 1                               | A+  | 9  | 9  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                      | -        | -        | 8 /10.0  | 15 /30.0             | 44 /60.0 | 67/100 | 67   | 3.0                             | 3   | B+ | 7  | 21  | --  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                      | -        | 17 /30.0 | -        | -                    | 17/30    | 57     | 1.0  | 1                               | B   | 6  | 6  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                      | -        | -        | 7 /10.0  | 20 /30.0             | 47 /60.0 | 74/100 | 74   | 3.0                             | 3   | A  | 8  | 24  | --  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                      | 18 /30.0 | -        | -        | -                    | 18/30    | 60     | 1.0  | 1                               | B+  | 7  | 7  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                      | -        | -        | 7 /10.0  | -                    | 44 /60.0 | 51/70  | 73   | 2.0                             | 2   | A  | 8  | 16  | --  |
| IT24053PR DATA STRUCTURE                                                         | *  | 27 /30.0                               | -        | -        | -        | -                    | 27/30    | 90     | 1.0  | 1                               | O   | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 21 /30.0                               | 25 /30.0 | -        | 7 /10.0  | -                    | 53/70    | 76     | 2.0  | 2                               | A   | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 26 /30.0                               | 24 /30.0 | -        | 9 /10.0  | -                    | 59/70    | 84     | 2.0  | 2                               | A+  | 9  | 18 | --  |     |
| SGPA1 : 8.36                                                                     |    |                                        |          |          |          |                      |          |        |      |                                 |     |    |    |     |     |
| CGPA : 8.69                                                                      |    | Cumulative Result Status : PASS        |          |          |          |                      |          |        |      | TOTAL EARNED CREDIT : 22.0/22.0 |     |    |    |     |     |
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| PRN NO. : EN24102034                                                             |   | NAME OF CANDIDATE : NAGDHARE PALLAVI MALLU |          |          |          | MOTHER NAME : LAXMIBAI |          |        |      |                                 |      |     |    |    |     |     |
|----------------------------------------------------------------------------------|---|--------------------------------------------|----------|----------|----------|------------------------|----------|--------|------|---------------------------------|------|-----|----|----|-----|-----|
| COURSE NAME                                                                      |   | TW                                         | OR       | PR       | CAA      | ISE                    | ESE      | Tot    | Tot% | C CR                            | E CR | Grd | GP | CP | P&R | ORD |
| SEM.: III                                                                        |   |                                            |          |          |          |                        |          |        |      |                                 |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | * | -                                          | -        | -        | 8 /10.0  | 27 /30.0               | 56 /60.0 | 91/100 | 91   | 3.0                             | 3    | O   | 10 | 30 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | * | -                                          | -        | -        | 9 /10.0  | 9 /30.0                | 26 /60.0 | 44/100 | 44   | 3.0                             | 3    | P   | 4  | 12 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | * | -                                          | -        | 21 /30.0 | -        | -                      | -        | 21/30  | 70   | 1.0                             | 1    | A   | 8  | 8  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | * | -                                          | -        | -        | 10 /10.0 | 17 /30.0               | 46 /60.0 | 73/100 | 73   | 3.0                             | 3    | A   | 8  | 24 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | * | -                                          | -        | 21 /30.0 | -        | -                      | -        | 21/30  | 70   | 1.0                             | 1    | A   | 8  | 8  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | * | -                                          | -        | -        | 7 /10.0  | 18 /30.0               | 32 /60.0 | 57/100 | 57   | 3.0                             | 3    | B   | 6  | 18 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | * | -                                          | 19 /30.0 | -        | -        | -                      | -        | 19/30  | 63   | 1.0                             | 1    | B+  | 7  | 7  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | * | -                                          | -        | -        | 7 /10.0  | -                      | 38 /60.0 | 45/70  | -    | 2.0                             | 2    | B+  | 7  | 14 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | * | 26 /30.0                                   | -        | -        | -        | -                      | -        | 26/30  | 87   | 1.0                             | 1    | A+  | 9  | 9  | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | * | 22 /30.0                                   | 24 /30.0 | -        | 8 /10.0  | -                      | -        | 54/70  | 77   | 2.0                             | 2    | A   | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | * | 27 /30.0                                   | 23 /30.0 | -        | 9 /10.0  | -                      | -        | 59/70  | 84   | 2.0                             | 2    | A+  | 9  | 18 | --  |     |
| SGPA1 : 7.45                                                                     |   |                                            |          |          |          |                        |          |        |      |                                 |      |     |    |    |     |     |
| CGPA : 7.88                                                                      |   | Cumulative Result Status : PASS            |          |          |          |                        |          |        |      | TOTAL EARNED CREDIT : 22.0/22.0 |      |     |    |    |     |     |
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| COURSE NAME                                                                                                                     | TW | OR       | PR       | CAA      | ISE      | ESE      | Tot      | Tot%   | C CR | E CR | Grd | GP | CP | P&R | ORD |
|---------------------------------------------------------------------------------------------------------------------------------|----|----------|----------|----------|----------|----------|----------|--------|------|------|-----|----|----|-----|-----|
| <div> <div>PRN NO. : EN24102039</div> <div>NAME OF CANDIDATE : NIPANE ARPITA ANKUSH</div> <div>MOTHER NAME : MANDA</div> </div> |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| SEM.: III                                                                                                                       |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                                                                             | *  | -        | -        | -        | 10 /10.0 | 21 /30.0 | 60 /60.0 | 91/100 | 91   | 3.0  | 3   | O  | 10 | 30  | --  |
| ET24201TH ANALOG CIRCUITS                                                                                                       | *  | -        | -        | -        | 9 /10.0  | 28 /30.0 | 49 /60.0 | 86/100 | 86   | 3.0  | 3   | A+ | 9  | 27  | --  |
| ET24201PR ANALOG CIRCUITS                                                                                                       | *  | -        | -        | 21 /30.0 | -        | -        | -        | 21/30  | 70   | 1.0  | 1   | A  | 8  | 8   | --  |
| ET24202TH DIGITAL LOGIC DESIGN                                                                                                  | *  | -        | -        | -        | 8 /10.0  | 20 /30.0 | 42 /60.0 | 70/100 | 70   | 3.0  | 3   | A  | 8  | 24  | --  |
| ET24202PR DIGITAL LOGIC DESIGN                                                                                                  | *  | -        | -        | 16 /30.0 | -        | -        | -        | 16/30  | 53   | 1.0  | 1   | B  | 6  | 6   | --  |
| ET24203TH SIGNALS AND SYSTEMS                                                                                                   | *  | -        | -        | -        | 7 /10.0  | 21 /30.0 | 46 /60.0 | 74/100 | 74   | 3.0  | 3   | A  | 8  | 24  | --  |
| ET24203PR SIGNALS AND SYSTEMS                                                                                                   | *  | -        | 18 /30.0 | -        | -        | -        | -        | 18/30  | 60   | 1.0  | 1   | B+ | 7  | 7   | --  |
| IT24053TH DATA STRUCTURE                                                                                                        | *  | -        | -        | -        | 8 /10.0  | -        | 55 /60.0 | 63/70  | 90   | 2.0  | 2   | O  | 10 | 20  | --  |
| IT24053PR DATA STRUCTURE                                                                                                        | *  | 28 /30.0 | -        | -        | -        | -        | -        | 28/30  | 93   | 1.0  | 1   | O  | 10 | 10  | --  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                                                                           | *  | 23 /30.0 | 26 /30.0 | -        | 7 /10.0  | -        | -        | 56/70  | 80   | 2.0  | 2   | A+ | 9  | 18  | --  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                                                                         | *  | 25 /30.0 | 25 /30.0 | -        | 8 /10.0  | -        | -        | 58/70  | 83   | 2.0  | 2   | A+ | 9  | 18  | --  |
| SGPA1 : 8.73                                                                                                                    |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| <div> <div>CGPA : 8.95</div> <div>Cumulative Result Status : PASS</div> <div>TOTAL EARNED CREDIT : 22.0/22.0</div> </div>       |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
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| PRN NO. : EN24102046                                                             |   | NAME OF CANDIDATE : PAWAR VAISHNAVI DNYANDEV |          |          |         | MOTHER NAME : SANGEETA |          |        |      |      |      |                                 |    |    |     |     |
|----------------------------------------------------------------------------------|---|----------------------------------------------|----------|----------|---------|------------------------|----------|--------|------|------|------|---------------------------------|----|----|-----|-----|
| COURSE NAME                                                                      |   | TW                                           | OR       | PR       | CAA     | ISE                    | ESE      | Tot    | Tot% | C CR | E CR | Grd                             | GP | CP | P&R | ORD |
| SEM.: III                                                                        |   |                                              |          |          |         |                        |          |        |      |      |      |                                 |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | * | -                                            | -        | -        | 8 /10.0 | 25 /30.0               | 60 /60.0 | 93/100 | 93   | 3.0  | 3    | O                               | 10 | 30 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | * | -                                            | -        | -        | 8 /10.0 | 20 /30.0               | 40 /60.0 | 68/100 | 68   | 3.0  | 3    | B+                              | 7  | 21 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | * | -                                            | -        | 13 /30.0 | -       | -                      | -        | 13/30  | 43   | 1.0  | 1    | P                               | 4  | 4  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | * | -                                            | -        | -        | 9 /10.0 | 13 /30.0               | 34 /60.0 | 56/100 | 56   | 3.0  | 3    | B                               | 6  | 18 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | * | -                                            | -        | 18 /30.0 | -       | -                      | -        | 18/30  | 60   | 1.0  | 1    | B+                              | 7  | 7  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | * | -                                            | -        | -        | 8 /10.0 | 26 /30.0               | 43 /60.0 | 77/100 | 77   | 3.0  | 3    | A                               | 8  | 24 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | * | -                                            | 23 /30.0 | -        | -       | -                      | -        | 23/30  | 77   | 1.0  | 1    | A                               | 8  | 8  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | * | -                                            | -        | -        | 9 /10.0 | -                      | 51 /60.0 | 60/70  | 86   | 2.0  | 2    | A+                              | 9  | 18 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | * | 29 /30.0                                     | -        | -        | -       | -                      | -        | 29/30  | 97   | 1.0  | 1    | O                               | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | * | 21 /30.0                                     | 25 /30.0 | -        | 8 /10.0 | -                      | -        | 54/70  | 77   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | * | 25 /30.0                                     | 24 /30.0 | -        | 8 /10.0 | -                      | -        | 57/70  | 81   | 2.0  | 2    | A+                              | 9  | 18 | --  |     |
| SGPA1 : 7.91                                                                     |   |                                              |          |          |         |                        |          |        |      |      |      |                                 |    |    |     |     |
| CGPA : 8.33                                                                      |   | Cumulative Result Status : PASS              |          |          |         |                        |          |        |      |      |      | TOTAL EARNED CREDIT : 22.0/22.0 |    |    |     |     |
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| PRN NO. : EN24102050                                                             |    | NAME OF CANDIDATE : RAMTEKE RAGINI SANJAY |          |          |          | MOTHER NAME : JYOTI |          |        |      |      |     |                                 |    |     |     |  |
|----------------------------------------------------------------------------------|----|-------------------------------------------|----------|----------|----------|---------------------|----------|--------|------|------|-----|---------------------------------|----|-----|-----|--|
| COURSE NAME                                                                      | TW | OR                                        | PR       | CAA      | ISE      | ESE                 | Tot      | Tot%   | C CR | E CR | Grd | GP                              | CP | P&R | ORD |  |
| SEM.: III                                                                        |    |                                           |          |          |          |                     |          |        |      |      |     |                                 |    |     |     |  |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                         | -        | -        | 10 /10.0 | 26 /30.0            | 53 /60.0 | 89/100 | 89   | 3.0  | 3   | A+                              | 9  | 27  | --  |  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                         | -        | -        | 8 /10.0  | 18 /30.0            | 16 /60.0 | 42/100 | 42   | 3.0  | 0   | F                               | 0  | 0   | --  |  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                         | -        | 15 /30.0 | -        | -                   | 15/30    | 50     | 1.0  | 1    | B   | 6                               | 6  | --  |     |  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | -        | 8 /10.0  | 25 /30.0            | 30 /60.0 | 63/100 | 63   | 3.0  | 3   | B+                              | 7  | 21  | --  |  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | 26 /30.0 | -        | -                   | 26/30    | 87     | 1.0  | 1    | A+  | 9                               | 9  | --  |     |  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                         | -        | -        | 6 /10.0  | 14 /30.0            | 27 /60.0 | 47/100 | 47   | 3.0  | 3   | C                               | 5  | 15  | --  |  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                         | 14 /30.0 | -        | -        | -                   | 14/30    | 47     | 1.0  | 1    | C   | 5                               | 5  | --  |     |  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                         | -        | -        | 6 /10.0  | -                   | 40 /60.0 | 46/70  | 66   | 2.0  | 2   | B+                              | 7  | 14  | --  |  |
| IT24053PR DATA STRUCTURE                                                         | *  | 26 /30.0                                  | -        | -        | -        | -                   | 26/30    | 87     | 1.0  | 1    | A+  | 9                               | 9  | --  |     |  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 20 /30.0                                  | 26 /30.0 | -        | 8 /10.0  | -                   | 54/70    | 77     | 2.0  | 2    | A   | 8                               | 16 | --  |     |  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 26 /30.0                                  | 24 /30.0 | -        | 8 /10.0  | -                   | 58/70    | 83     | 2.0  | 2    | A+  | 9                               | 18 | --  |     |  |
| SGPA1 : -                                                                        |    |                                           |          |          |          |                     |          |        |      |      |     |                                 |    |     |     |  |
| CGPA :                                                                           |    | Cumulative Result Status : A.T.K.T.       |          |          |          |                     |          |        |      |      |     | TOTAL EARNED CREDIT : 19.0/22.0 |    |     |     |  |
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| COURSE NAME                                                                      | TW | OR       | PR       | CAA      | ISE      | ESE      | Tot    | Tot% | C CR | E CR | Grd | GP | CP | P&R | ORD |
|----------------------------------------------------------------------------------|----|----------|----------|----------|----------|----------|--------|------|------|------|-----|----|----|-----|-----|
| SEM.: III                                                                        |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -        | -        | 10 /10.0 | 13 /30.0 | 27 /60.0 | 50/100 | 50   | 3.0  | 3    | B   | 6  | 18 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -        | -        | 7 /10.0  | 9 /30.0  | 26 /60.0 | 42/100 | 42   | 3.0  | 3    | P   | 4  | 12 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -        | 16 /30.0 | -        | -        | -        | 16/30  | 53   | 1.0  | 1    | B   | 6  | 6  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -        | -        | 8 /10.0  | 5 /30.0  | 32 /60.0 | 45/100 | -    | 3.0  | 3    | C   | 5  | 15 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -        | 20 /30.0 | -        | -        | -        | 20/30  | 67   | 1.0  | 1    | B+  | 7  | 7  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -        | -        | 4 /10.0  | 15 /30.0 | 28 /60.0 | 47/100 | 47   | 3.0  | 3    | C   | 5  | 15 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -        | 12 /30.0 | -        | -        | -        | 12/30  | 40   | 1.0  | 1    | P   | 4  | 4  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | *  | -        | -        | 6 /10.0  | -        | 30 /60.0 | 36/70  | 51   | 2.0  | 2    | B   | 6  | 12 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | *  | 21 /30.0 | -        | -        | -        | -        | 21/30  | 70   | 1.0  | 1    | A   | 8  | 8  | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 20 /30.0 | 24 /30.0 | -        | 7 /10.0  | -        | 51/70  | 73   | 2.0  | 2    | A   | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 27 /30.0 | 23 /30.0 | -        | 8 /10.0  | -        | 58/70  | 83   | 2.0  | 2    | A+  | 9  | 18 | --  |     |
| SGPA1 : 5.95                                                                     |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| CGPA : 6.27                                                                      |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| Cumulative Result Status : PASS                                                  |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| TOTAL EARNED CREDIT : 22.0/22.0                                                  |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
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| PRN NO. : EN25202001                                                             |    | NAME OF CANDIDATE : CHINCHKAR TEJAS VIJAY |          |          |         | MOTHER NAME : TEJASWINI |          |        |      |      |     |                                 |    |     |     |
|----------------------------------------------------------------------------------|----|-------------------------------------------|----------|----------|---------|-------------------------|----------|--------|------|------|-----|---------------------------------|----|-----|-----|
| COURSE NAME                                                                      | TW | OR                                        | PR       | CAA      | ISE     | ESE                     | Tot      | Tot%   | C CR | E CR | Grd | GP                              | CP | P&R | ORD |
| SEM.: III                                                                        |    |                                           |          |          |         |                         |          |        |      |      |     |                                 |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -                                         | -        | -        | 9 /10.0 | 14 /30.0                | 29 /60.0 | 52/100 | 52   | 3.0  | 3   | B                               | 6  | 18  | --  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -                                         | -        | -        | 9 /10.0 | 24 /30.0                | 37 /60.0 | 70/100 | 70   | 3.0  | 3   | A                               | 8  | 24  | --  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -                                         | -        | 19 /30.0 | -       | -                       | -        | 19/30  | 63   | 1.0  | 1   | B+                              | 7  | 7   | --  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | -        | 9 /10.0 | 29 /30.0                | 30 /60.0 | 68/100 | 68   | 3.0  | 3   | B+                              | 7  | 21  | --  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -                                         | -        | 24 /30.0 | -       | -                       | -        | 24/30  | 80   | 1.0  | 1   | A+                              | 9  | 9   | --  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -                                         | -        | -        | 7 /10.0 | 14 /30.0                | 33 /60.0 | 54/100 | 54   | 3.0  | 3   | B                               | 6  | 18  | --  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -                                         | 21 /30.0 | -        | -       | -                       | -        | 21/30  | 70   | 1.0  | 1   | A                               | 8  | 8   | --  |
| IT24053TH DATA STRUCTURE                                                         | *  | -                                         | -        | -        | 8 /10.0 | -                       | 46 /60.0 | 54/70  | 77   | 2.0  | 2   | A                               | 8  | 16  | --  |
| IT24053PR DATA STRUCTURE                                                         | *  | 28 /30.0                                  | -        | -        | -       | -                       | -        | 28/30  | 93   | 1.0  | 1   | O                               | 10 | 10  | --  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 21 /30.0                                  | 24 /30.0 | -        | 8 /10.0 | -                       | -        | 53/70  | 76   | 2.0  | 2   | A                               | 8  | 16  | --  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 26 /30.0                                  | 22 /30.0 | -        | 8 /10.0 | -                       | -        | 56/70  | 80   | 2.0  | 2   | A+                              | 9  | 18  | --  |
| SGPA1 : 7.5                                                                      |    |                                           |          |          |         |                         |          |        |      |      |     |                                 |    |     |     |
| CGPA : 7.5                                                                       |    | Cumulative Result Status : PASS           |          |          |         |                         |          |        |      |      |     | TOTAL EARNED CREDIT : 22.0/22.0 |    |     |     |
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| COURSE NAME                                                                      | TW | OR       | PR       | CAA      | ISE      | ESE      | Tot      | Tot%   | C CR | E CR | Grd | GP | CP | P&R | ORD |
|----------------------------------------------------------------------------------|----|----------|----------|----------|----------|----------|----------|--------|------|------|-----|----|----|-----|-----|
| SEM.: III                                                                        |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | *  | -        | -        | -        | 10 /10.0 | 28 /30.0 | 58 /60.0 | 96/100 | 96   | 3.0  | 3   | O  | 10 | 30  | --  |
| ET24201TH ANALOG CIRCUITS                                                        | *  | -        | -        | -        | 9 /10.0  | 25 /30.0 | 51 /60.0 | 85/100 | 85   | 3.0  | 3   | A+ | 9  | 27  | --  |
| ET24201PR ANALOG CIRCUITS                                                        | *  | -        | -        | 21 /30.0 | -        | -        | -        | 21/30  | 70   | 1.0  | 1   | A  | 8  | 8   | --  |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | *  | -        | -        | -        | 10 /10.0 | 30 /30.0 | 53 /60.0 | 93/100 | 93   | 3.0  | 3   | O  | 10 | 30  | --  |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | *  | -        | -        | 22 /30.0 | -        | -        | -        | 22/30  | 73   | 1.0  | 1   | A  | 8  | 8   | --  |
| ET24203TH SIGNALS AND SYSTEMS                                                    | *  | -        | -        | -        | 8 /10.0  | 27 /30.0 | 45 /60.0 | 80/100 | 80   | 3.0  | 3   | A+ | 9  | 27  | --  |
| ET24203PR SIGNALS AND SYSTEMS                                                    | *  | -        | 22 /30.0 | -        | -        | -        | -        | 22/30  | 73   | 1.0  | 1   | A  | 8  | 8   | --  |
| IT24053TH DATA STRUCTURE                                                         | *  | -        | -        | -        | 10 /10.0 | -        | 58 /60.0 | 68/70  | 97   | 2.0  | 2   | O  | 10 | 20  | --  |
| IT24053PR DATA STRUCTURE                                                         | *  | 29 /30.0 | -        | -        | -        | -        | -        | 29/30  | 97   | 1.0  | 1   | O  | 10 | 10  | --  |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | *  | 22 /30.0 | 24 /30.0 | -        | 9 /10.0  | -        | -        | 55/70  | 79   | 2.0  | 2   | A  | 8  | 16  | --  |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | *  | 24 /30.0 | 23 /30.0 | -        | 9 /10.0  | -        | -        | 56/70  | 80   | 2.0  | 2   | A+ | 9  | 18  | --  |
| SGPA1 : 9.18                                                                     |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| CGPA : 9.18                                                                      |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| Cumulative Result Status : PASS                                                  |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
| TOTAL EARNED CREDIT : 22.0/22.0                                                  |    |          |          |          |          |          |          |        |      |      |     |    |    |     |     |
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|                                                                                  |   |                                            |          |          |          |                      |          |        |      |      |      |                                 |    |    |     |     |
|----------------------------------------------------------------------------------|---|--------------------------------------------|----------|----------|----------|----------------------|----------|--------|------|------|------|---------------------------------|----|----|-----|-----|
| PRN NO. : EN25202009                                                             |   | NAME OF CANDIDATE : PATIL SANDHYA RAJENDRA |          |          |          | MOTHER NAME : SUNITA |          |        |      |      |      |                                 |    |    |     |     |
| COURSE NAME                                                                      |   | TW                                         | OR       | PR       | CAA      | ISE                  | ESE      | Tot    | Tot% | C CR | E CR | Grd                             | GP | CP | P&R | ORD |
| SEM.: III                                                                        |   |                                            |          |          |          |                      |          |        |      |      |      |                                 |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING                              | * | -                                          | -        | -        | 10 /10.0 | 30 /30.0             | 58 /60.0 | 98/100 | 98   | 3.0  | 3    | O                               | 10 | 30 | --  |     |
| ET24201TH ANALOG CIRCUITS                                                        | * | -                                          | -        | -        | 8 /10.0  | 22 /30.0             | 38 /60.0 | 68/100 | 68   | 3.0  | 3    | B+                              | 7  | 21 | --  |     |
| ET24201PR ANALOG CIRCUITS                                                        | * | -                                          | -        | 25 /30.0 | -        | -                    | -        | 25/30  | 83   | 1.0  | 1    | A+                              | 9  | 9  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                                                   | * | -                                          | -        | -        | 9 /10.0  | 21 /30.0             | 42 /60.0 | 72/100 | 72   | 3.0  | 3    | A                               | 8  | 24 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                                                   | * | -                                          | -        | 24 /30.0 | -        | -                    | -        | 24/30  | 80   | 1.0  | 1    | A+                              | 9  | 9  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                                                    | * | -                                          | -        | -        | 6 /10.0  | 20 /30.0             | 45 /60.0 | 71/100 | 71   | 3.0  | 3    | A                               | 8  | 24 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                                                    | * | -                                          | 19 /30.0 | -        | -        | -                    | -        | 19/30  | 63   | 1.0  | 1    | B+                              | 7  | 7  | --  |     |
| IT24053TH DATA STRUCTURE                                                         | * | -                                          | -        | -        | 10 /10.0 | -                    | 60 /60.0 | 70/70  | 100  | 2.0  | 2    | O                               | 10 | 20 | --  |     |
| IT24053PR DATA STRUCTURE                                                         | * | 29 /30.0                                   | -        | -        | -        | -                    | -        | 29/30  | 97   | 1.0  | 1    | O                               | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)                                            | * | 21 /30.0                                   | 23 /30.0 | -        | 7 /10.0  | -                    | -        | 51/70  | 73   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT                                          | * | 25 /30.0                                   | 22 /30.0 | -        | 8 /10.0  | -                    | -        | 55/70  | 79   | 2.0  | 2    | A                               | 8  | 16 | --  |     |
| SGPA1 : 8.45                                                                     |   |                                            |          |          |          |                      |          |        |      |      |      |                                 |    |    |     |     |
| CGPA : 8.45                                                                      |   | Cumulative Result Status : PASS            |          |          |          |                      |          |        |      |      |      | TOTAL EARNED CREDIT : 22.0/22.0 |    |    |     |     |
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| COURSE NAME                                         | TW | OR       | PR       | CAA      | ISE      | ESE      | Tot    | Tot% | C CR | E CR | Grd | GP | CP | P&R | ORD |
|-----------------------------------------------------|----|----------|----------|----------|----------|----------|--------|------|------|------|-----|----|----|-----|-----|
| SEM.: III                                           |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| BS24202TH ADVANCED MATHEMATICS FOR E&TC ENGINEERING | *  | -        | -        | 10 /10.0 | 30 /30.0 | 59 /60.0 | 99/100 | 99   | 3.0  | 3    | O   | 10 | 30 | --  |     |
| ET24201TH ANALOG CIRCUITS                           | *  | -        | -        | 9 /10.0  | 26 /30.0 | 48 /60.0 | 83/100 | 83   | 3.0  | 3    | A+  | 9  | 27 | --  |     |
| ET24201PR ANALOG CIRCUITS                           | *  | -        | 18 /30.0 | -        | -        | -        | 18/30  | 60   | 1.0  | 1    | B+  | 7  | 7  | --  |     |
| ET24202TH DIGITAL LOGIC DESIGN                      | *  | -        | -        | 9 /10.0  | 29 /30.0 | 50 /60.0 | 88/100 | 88   | 3.0  | 3    | A+  | 9  | 27 | --  |     |
| ET24202PR DIGITAL LOGIC DESIGN                      | *  | -        | 25 /30.0 | -        | -        | -        | 25/30  | 83   | 1.0  | 1    | A+  | 9  | 9  | --  |     |
| ET24203TH SIGNALS AND SYSTEMS                       | *  | -        | -        | 7 /10.0  | 25 /30.0 | 39 /60.0 | 71/100 | 71   | 3.0  | 3    | A   | 8  | 24 | --  |     |
| ET24203PR SIGNALS AND SYSTEMS                       | *  | -        | 17 /30.0 | -        | -        | -        | 17/30  | 57   | 1.0  | 1    | B   | 6  | 6  | --  |     |
| IT24053TH DATA STRUCTURE                            | *  | -        | -        | 10 /10.0 | -        | 60 /60.0 | 70/70  | 100  | 2.0  | 2    | O   | 10 | 20 | --  |     |
| IT24053PR DATA STRUCTURE                            | *  | 29 /30.0 | -        | -        | -        | -        | 29/30  | 97   | 1.0  | 1    | O   | 10 | 10 | --  |     |
| ET24204PR PROGRAMMING IN PYTHON (VSC)               | *  | 21 /30.0 | 23 /30.0 | -        | 9 /10.0  | -        | 53/70  | 76   | 2.0  | 2    | A   | 8  | 16 | --  |     |
| ET24205PR COMMUNITY ENGINEERING PROJECT             | *  | 25 /30.0 | 22 /30.0 | -        | 8 /10.0  | -        | 55/70  | 79   | 2.0  | 2    | A   | 8  | 16 | --  |     |
| SGPA1 : 8.73                                        |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| CGPA : 8.73                                         |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| Cumulative Result Status : PASS                     |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |
| TOTAL EARNED CREDIT : 22.0/22.0                     |    |          |          |          |          |          |        |      |      |      |     |    |    |     |     |

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